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[Fig. 2]

- A1: carrier wave
- A2: maximum value of carrier wave
- A3: PWM output
- A4: positive side
- A5: negative side

[Fig. 3]

- A1: modulation wave production

[Fig. 4]

- A1: real modulation ratio
- A2: modulation ratio instruction
- A3: ideal value
- A4: Sinusoidal instruction
- A5: 2-phase modulation
- A6: space vector

[Fig. 5]

- A1: real modulation ratio
- A2: overmodulation with correction
- A3: overmodulation without correction
- A4: limit value in conventional art
- A5: modulation ratio instruction

[Fig. 6]

A1: negative side pulse
A2: drive power source voltage
A3: lower limit
A4: time

[Fig. 7]

A1: upper limit value of modulation wave
A2: maximum value of carrier wave
A3: upper limit of minimum pulse width
A4: initial value of upper limit of modulation wave
A5: output frequency

[Fig. 9]

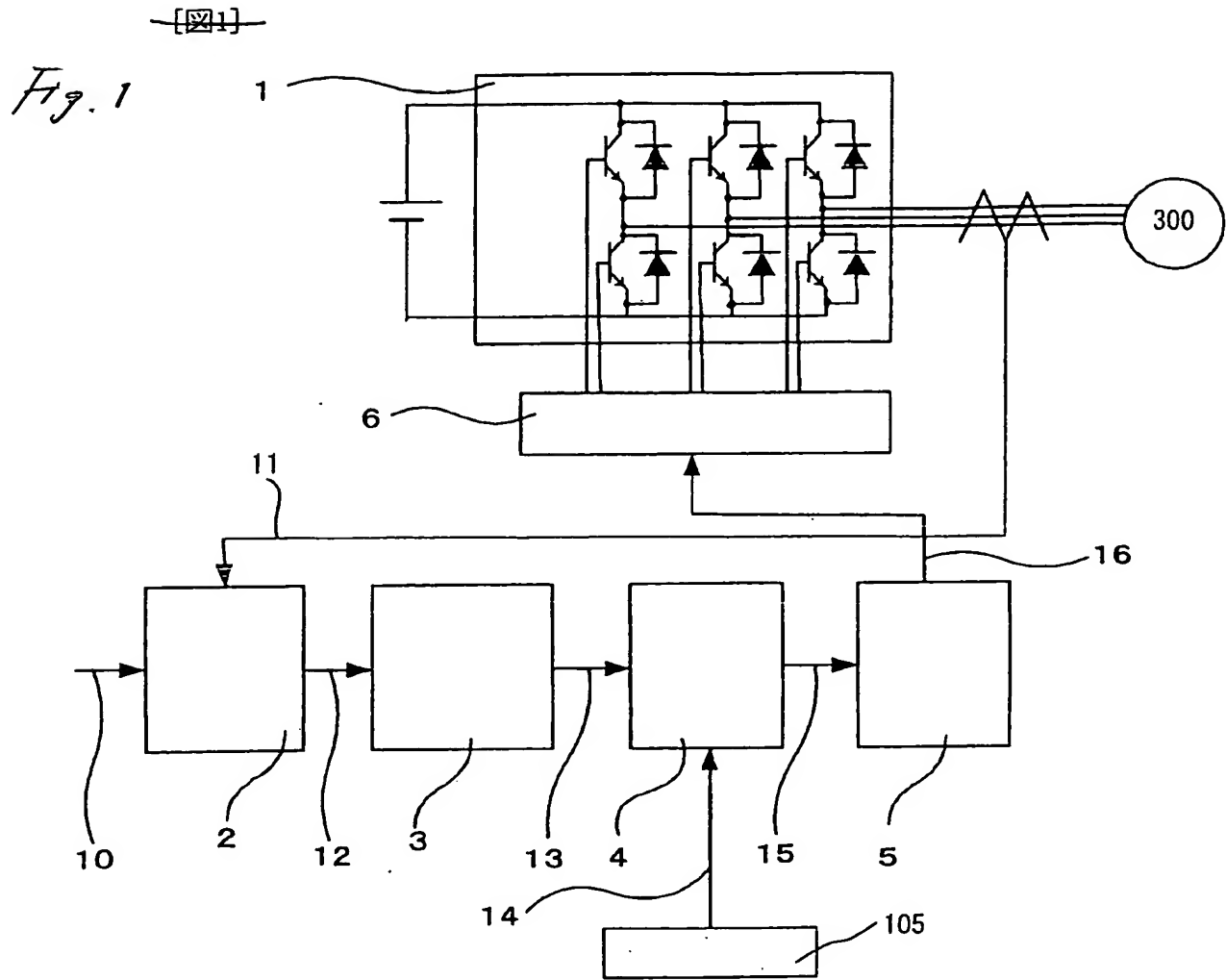
A1: PWM output
A2: positive side
A3: negative side

[Fig. 11]

A1: drive power source voltage

[Fig. 12]

A1: U phase current
A2: V phase current



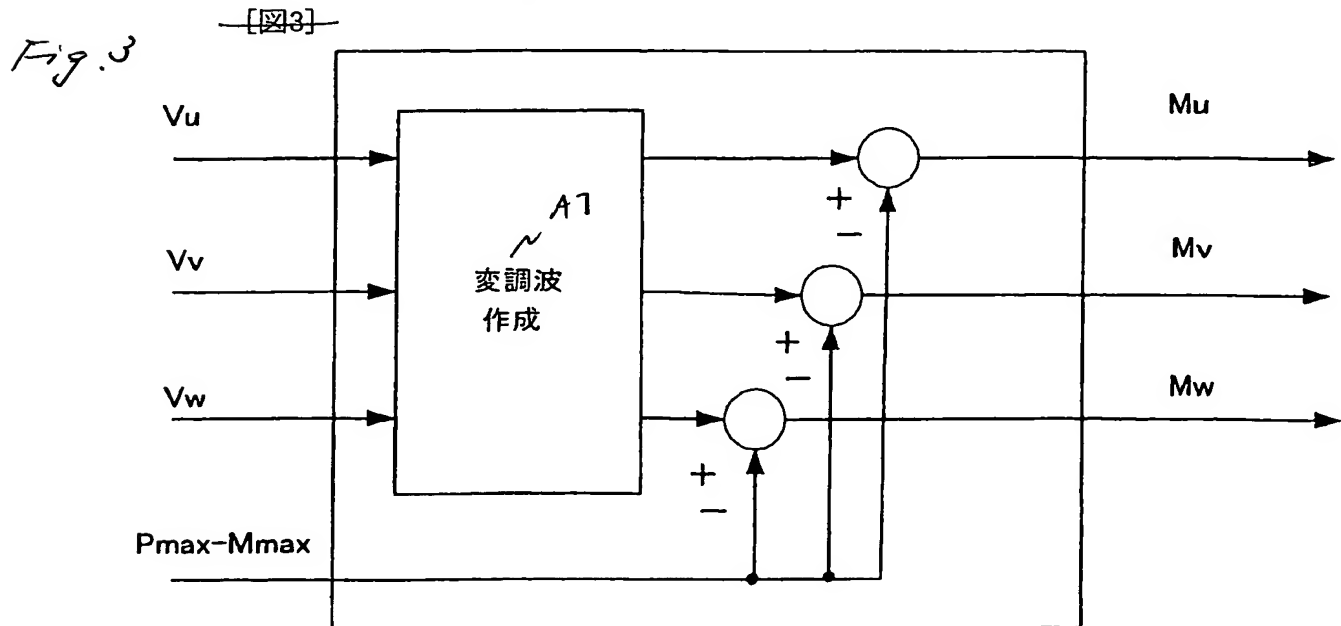
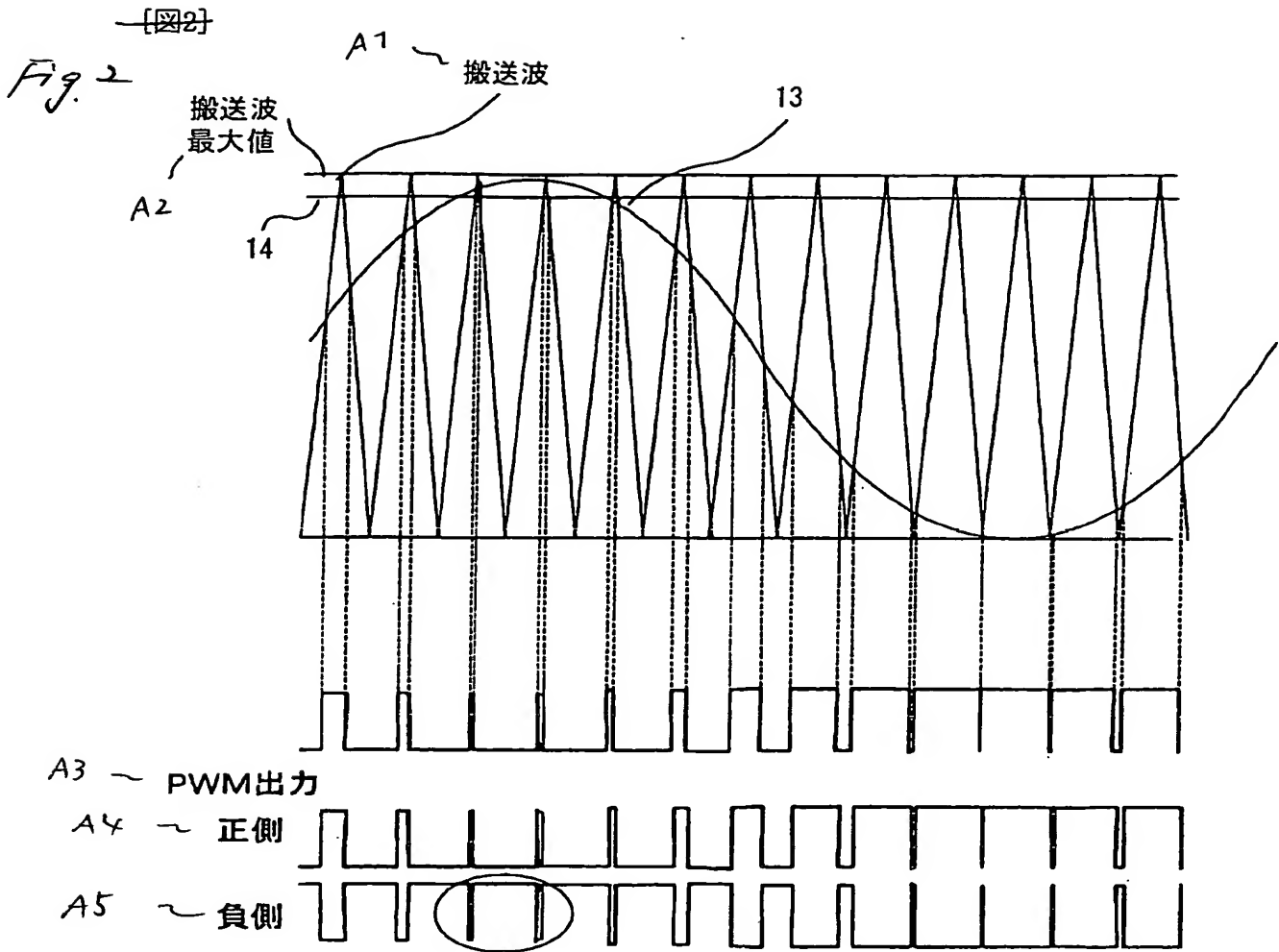
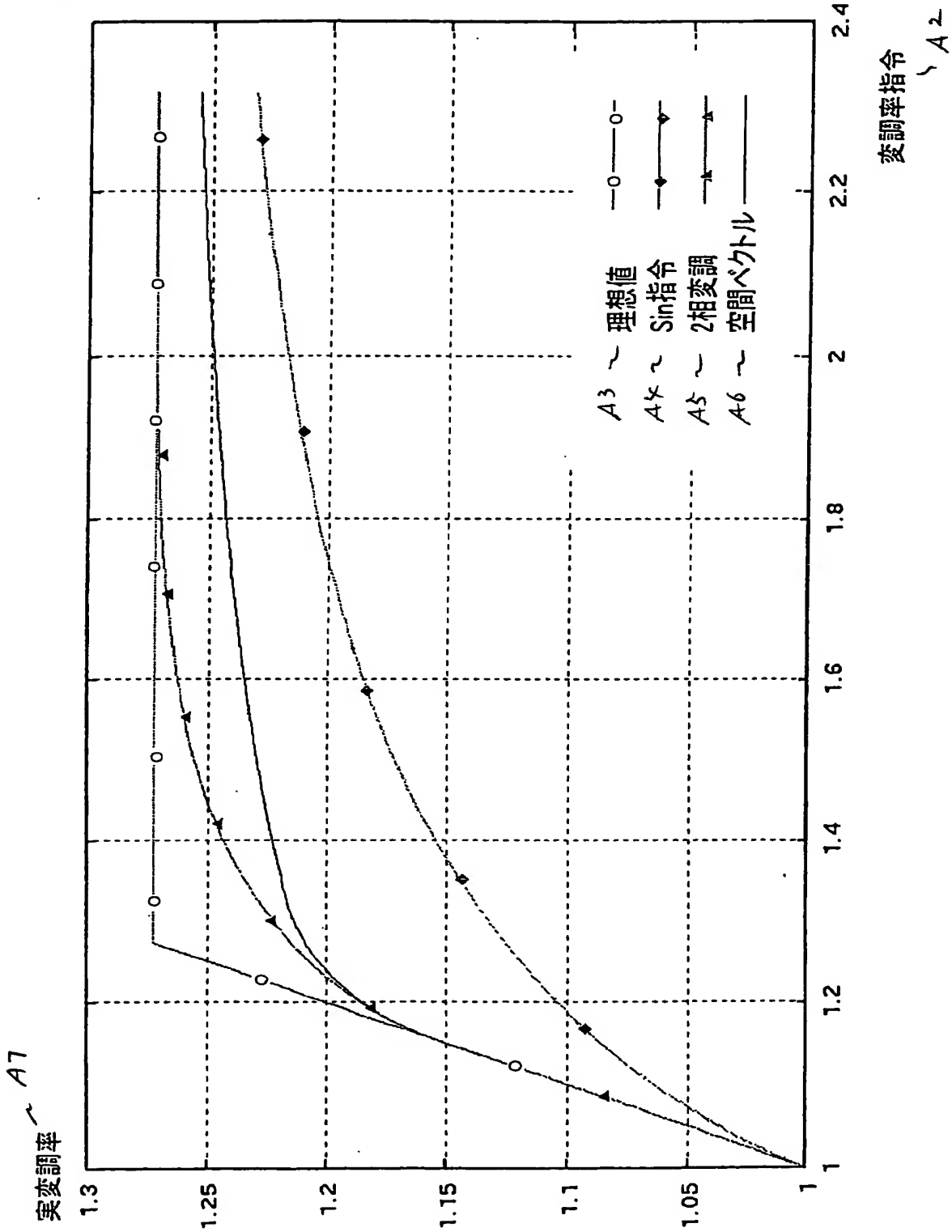


図4



[図5]
 Fig. 5 実変調率 A1

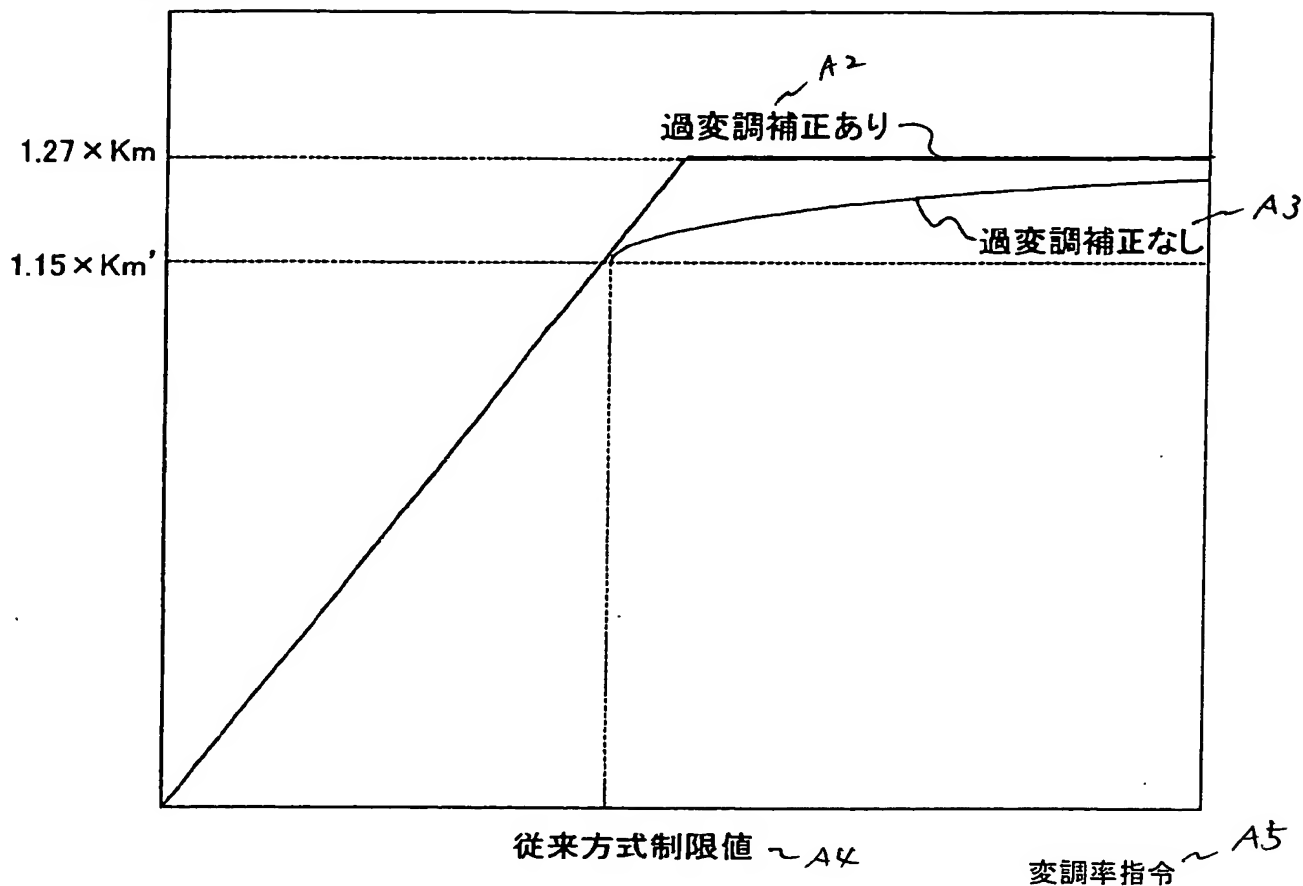
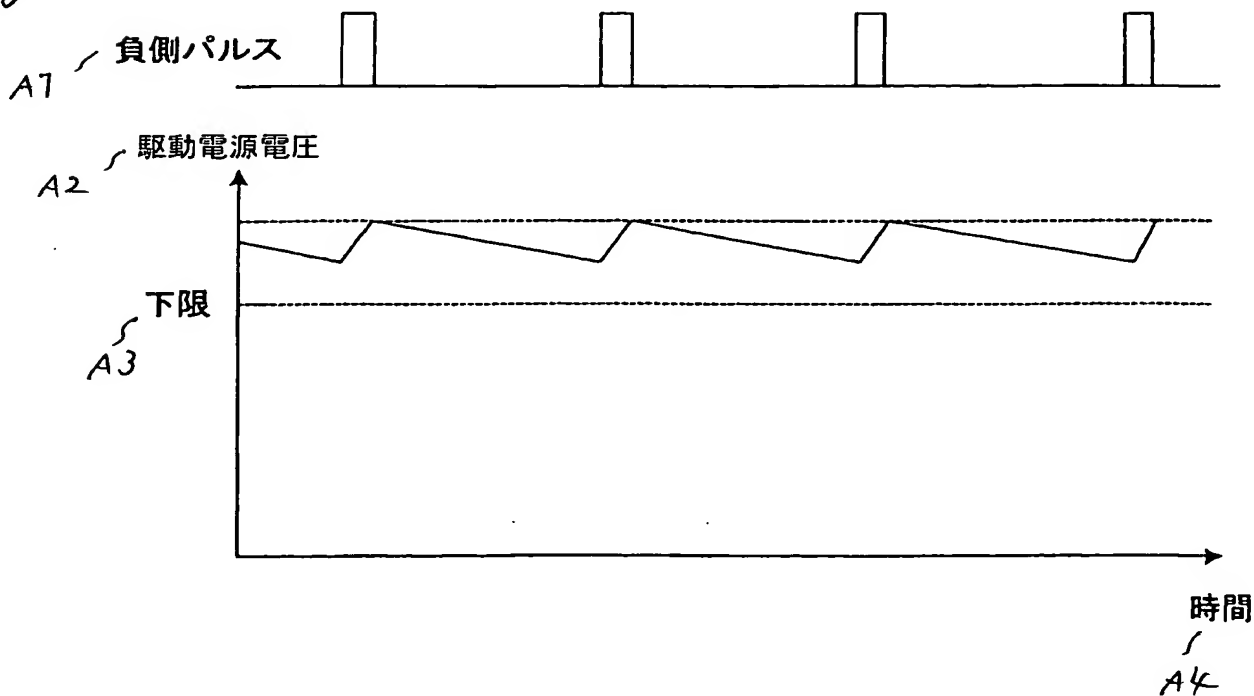
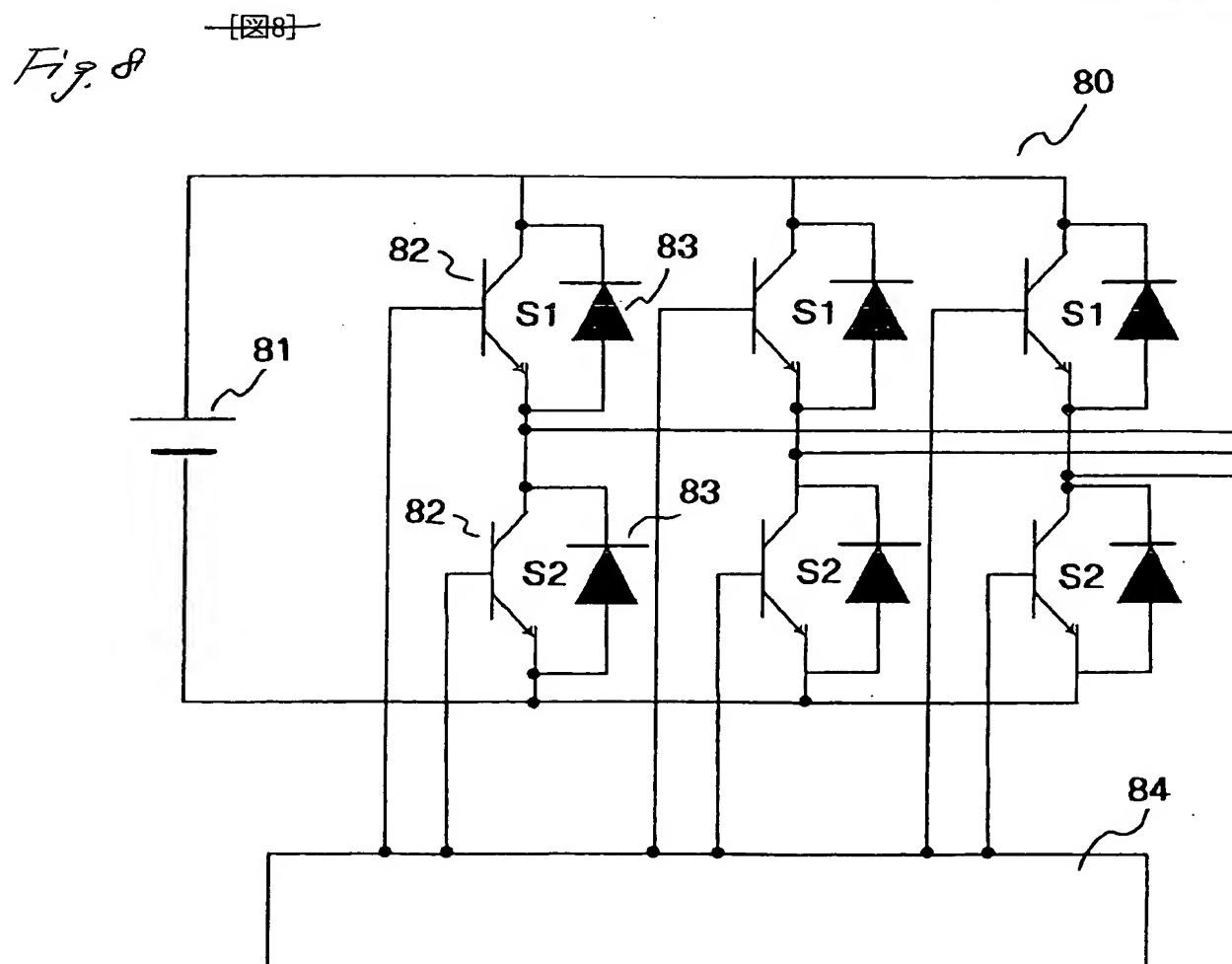
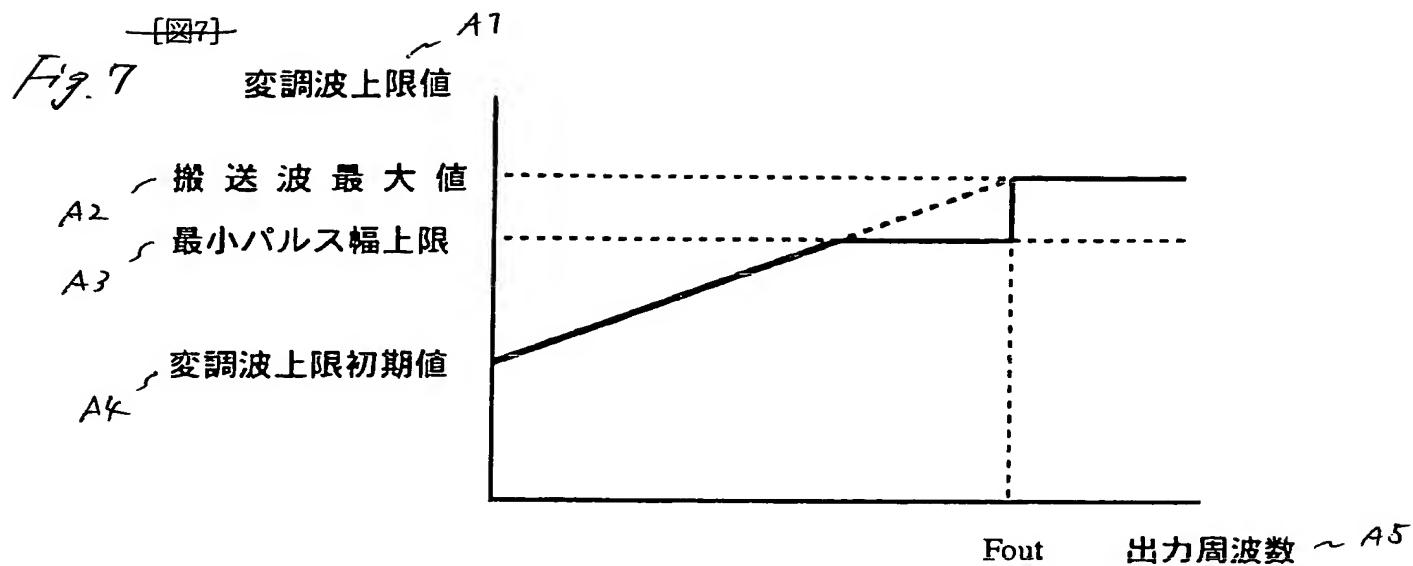


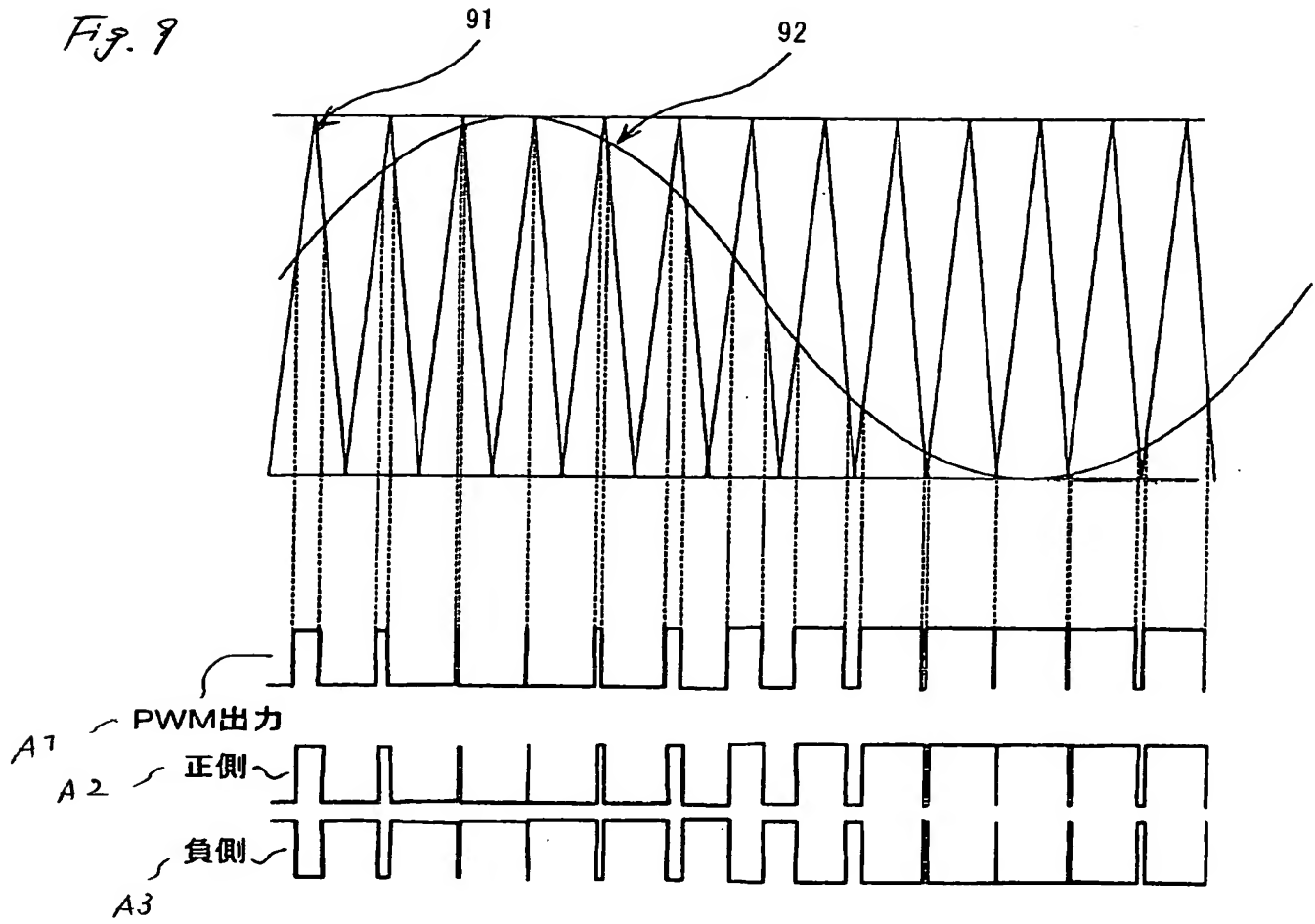
Fig. 6 [図6]





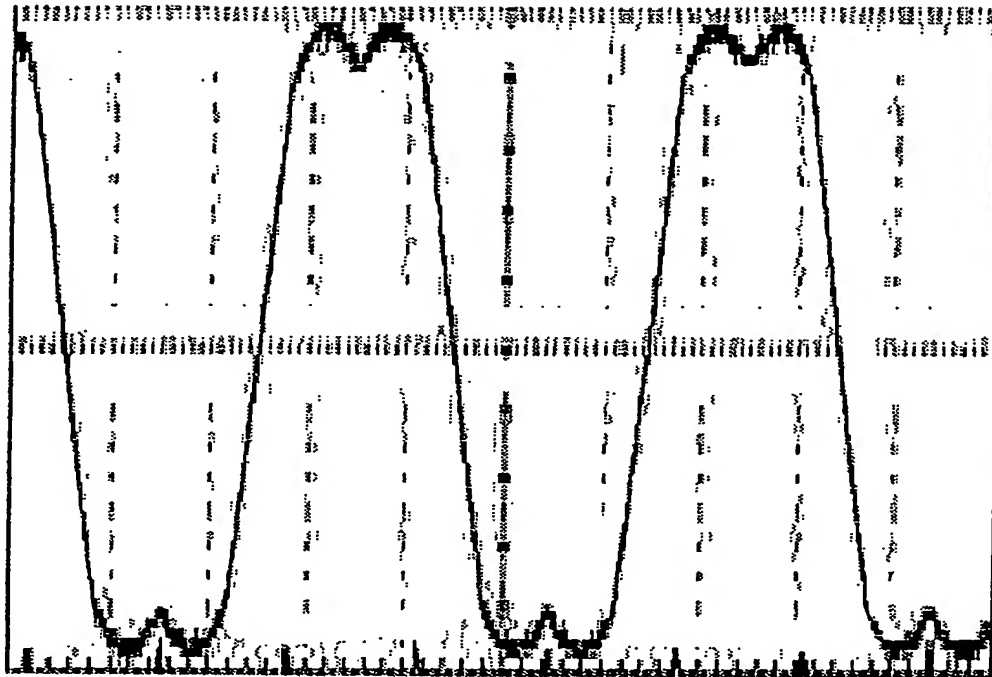
—[図9]—

Fig. 9



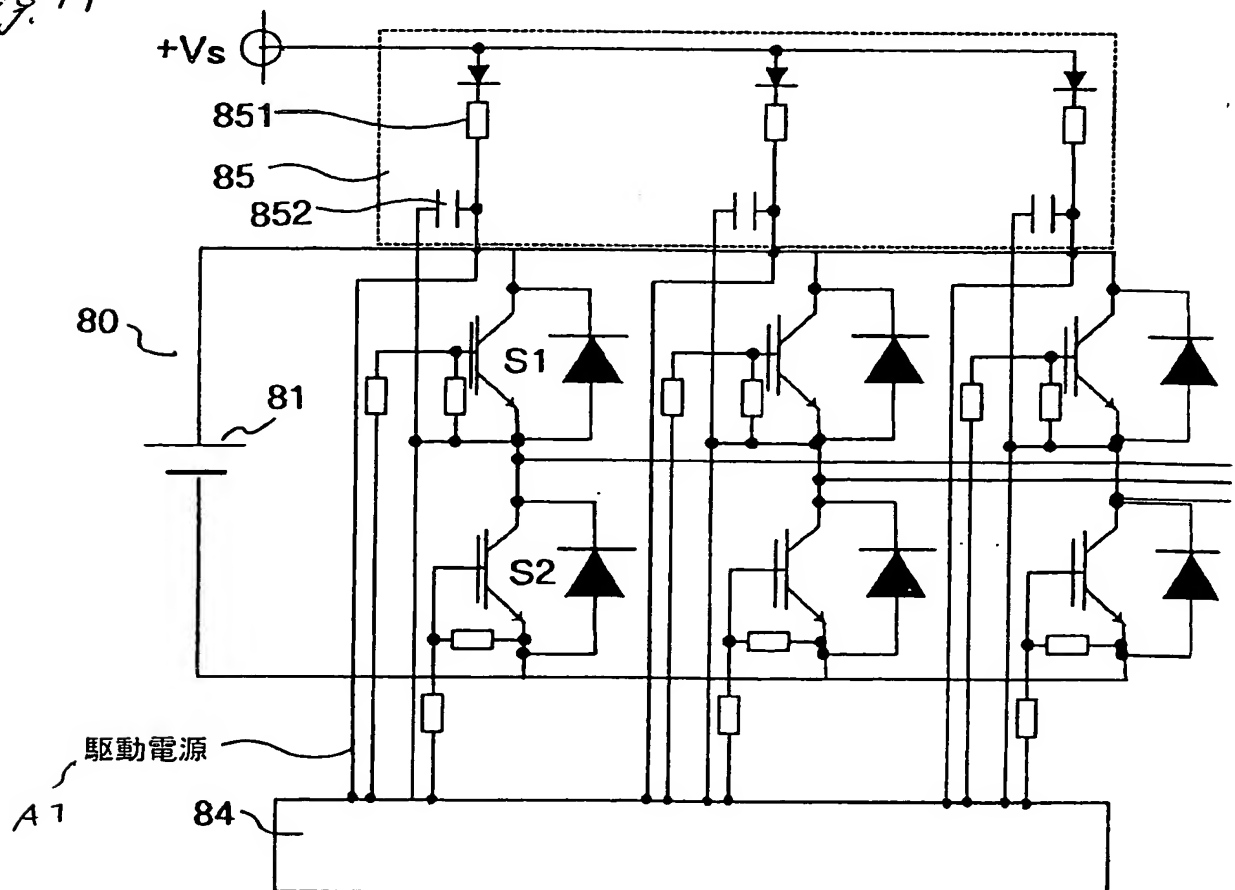
—[図10]—

Fig. 10



—[図11]—

Fig. 11



~~[図12]~~

Fig. 12

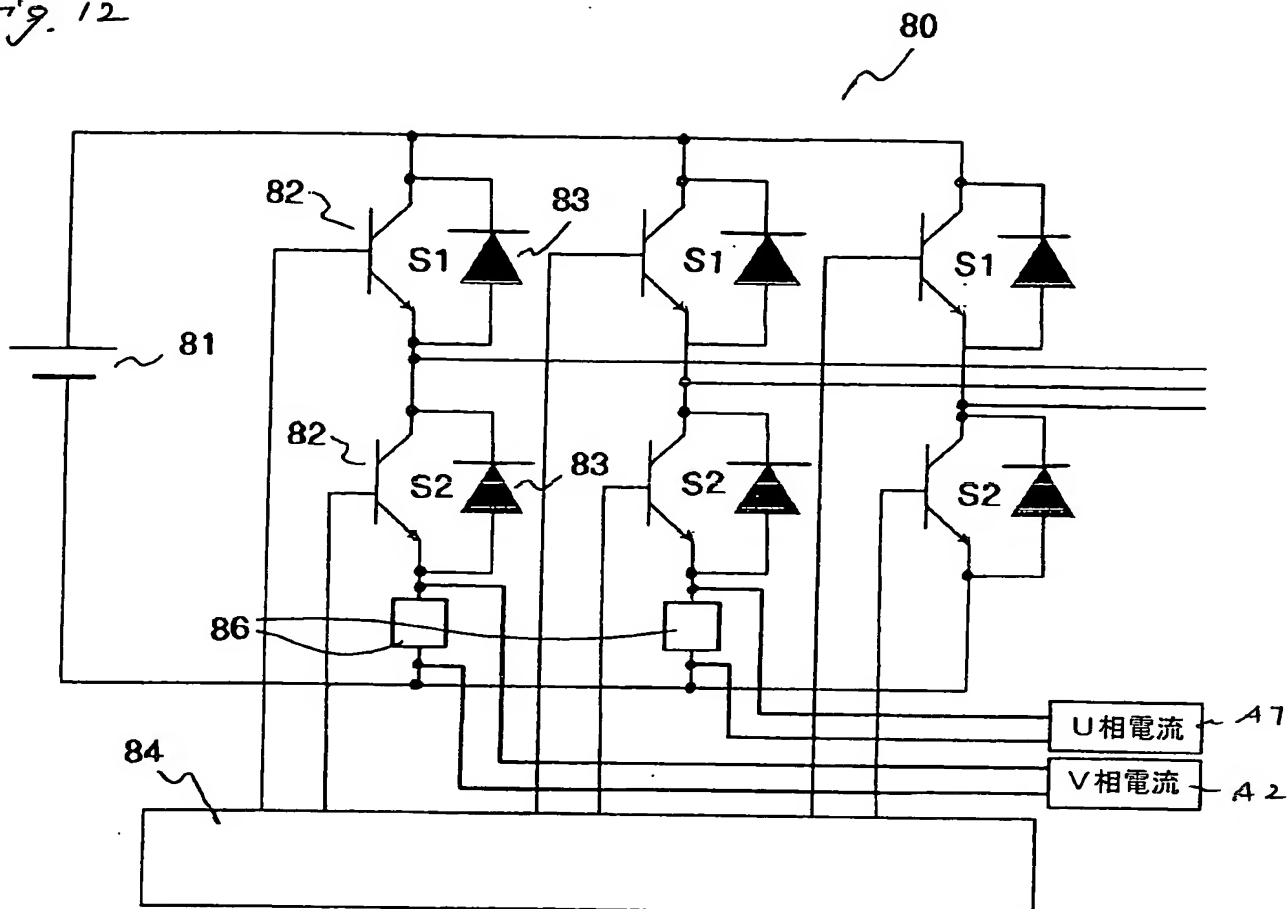
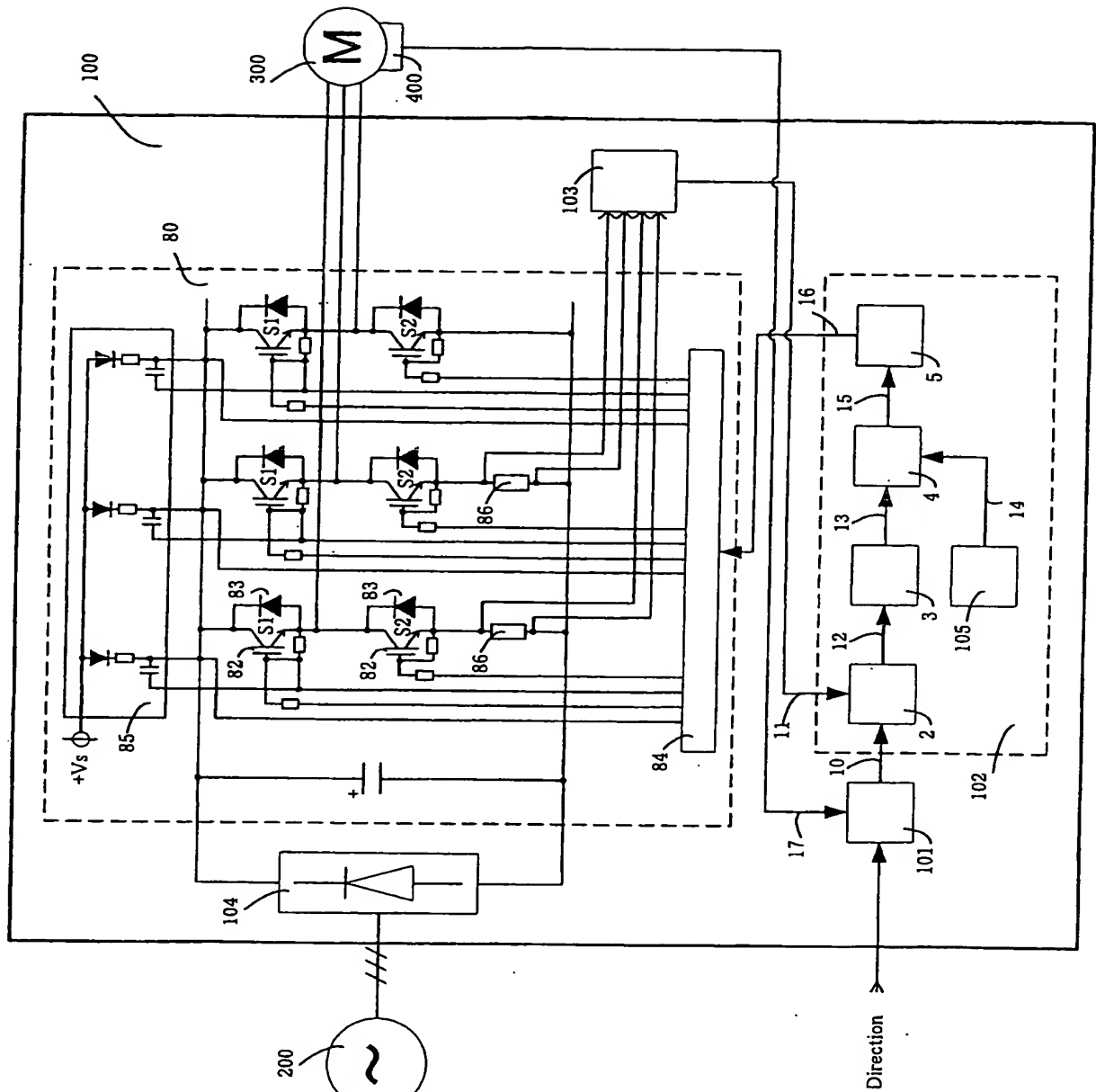


Fig. 13

[図13]



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